

**AMENDMENTS TO THE CLAIMS**

**This listing of claims will replace all prior versions and listings of claims in the application:**

**LISTING OF CLAIMS:**

1. (previously presented): Receiver device for optical data signals, in particular optical data signals in the Gb/s range, comprising:
  - an opto-electrical conversion unit, which converts an optical signal, that is received from a source external to said receiver device, to a converted electrical data signal;
  - a frequency multiplier unit, which frequency-multiplies the converted electrical data signal, thereby producing a frequency-multiplied signal; and
  - a clock recovery unit comprising a phased locked loop circuit,
    - wherein the frequency-multiplied signal is used to drive the phase locked loop circuit;
    - wherein the frequency multiplier unit performs a frequency multiplication by a factor of  $n$ , with  $n$  being a natural number larger than 2,
    - wherein the receiver device comprises a frequency filter for the spectral power of the electrical data signal,
    - wherein the frequency filter transmits around  $B/n$ , wherein  $B$  is the bit rate of the electrical data signal, and
    - wherein said frequency multiplication is an analog signal processing technique.
2. (canceled).
3. (original): Receiver device according to claim 1, wherein  $n=4$ .

4. (previously presented): Receiver device according to claim 1, wherein the optical data signals are 10 Gb/s signals or 40 Gb/s signals.

5. (original): Receiver device according to claim 1, wherein the clock recovery unit comprises a phase locked loop circuit.

6. (original): Receiver device according to claim 1, wherein the clock recovery unit comprises a filter clock recovery circuit.

7. (original): Data transmission system comprising an optical transmission link, in particular an optical fiber system, wherein the optical transmission link has a significant dispersion, and a receiver device according to claim 1.

8. (canceled)